carrying out the invention. Details of the structure may be varied substantially without departing from the spirit of the invention and the exclusive use of all modifications which come within the scope of the appended claim is reserved.

What is claimed:

1. A panel display comprising a plurality of display units that form a display area, each said display unit having video random access memory (VRAM) for storing image data, a controller for processing broadcast data signals to provide said image data to said VRAM, and an image display panel for displaying said image data, wherein said controller for each said display unit is operable to accept concurrent broadcast data signals from multiple sources intended for said plurality of display units that form said display area, each said display unit being operable to process image information with respect to said display area independently from said multiple sources, calculate a relative location for each said display unit relative to other display units within said plurality of display units that form said display area, and extract an applicable portion of said image data from said broadcast data signals based upon said relative location.

2. The panel display of claim **1** wherein said display area is substantially rectangular.

3. The panel display of claim **2** wherein said display area is electrically coupled to a plurality of source devices.

4. The panel display of claim **3** wherein said plurality of source devices are selected from the group consisting of computers, cable TV adapters, broadcast adapters, camcorders, and digital cameras.

5. The panel display of claim **3** wherein each said display unit further comprises a bus for transporting power, control signals and data signals from said plurality of source devices to said display unit controller.

6. The panel display of claim **5** wherein each said display further comprises a data/control port to connect said bus to said plurality of source devices.

7. The panel display of claim **6** wherein said controller calculates said relative location for each said display unit relative to other display units within said plurality of display units that form said display area by assigning a set of coordinates to the position of each display unit in said display area.

8. The panel display of claim **7** wherein said relative location is determined by comparing said coordinates to find a minimum and a maximum X coordinate and a minimum and a maximum Y coordinate, and using said minimum and maximum X and Y coordinates to find a smallest rectangle containing the entire display area.

9. The panel display of claim **8** wherein said image display panels provide substantially the same pixel resolution.

10. The panel display of claim **2** wherein said formation of display units is electrically coupled to at least one I/O device.

11. The panel display of claim **10** wherein said I/O device is selected from the group consisting of network devices, pointer devices, keyboards, touch screens, printers, and wireless connectors.

12. The panel display as in claim **2** wherein said image display panel is flat.

13. The panel display as in claim **2** wherein said image display panel is a liquid crystal display.

14. The panel display as in claim **2** wherein said image display panel is a light emitting diode.

15. The panel display as in claim **2** further comprising a nonvolatile memory to store information for said image display panel.

16. The panel display as in claim **15** wherein said nonvolatile memory is erasable programmable read only memory (EPROM).

17. The panel display as in claim **15** wherein said nonvolatile memory is flash memory.

18. The panel display as in claim **2** wherein said controller is an embedded processor.

19. A panel display system comprising:

a plurality of display units forming a display area, each said display unit having a video random access memory (VRAM) for storing image data, a controller for processing data signals to provide said image data to said VRAM, and an image display panel for displaying said image data; and

means for interconnecting said plurality of display units to form a non-overlapping display area,

wherein said controller for each said display unit is operable to accept concurrent broadcast data signals from multiple sources intended for said plurality of display units that form said display area, each said display unit being operable to process image information with respect to said display area independently from said multiple sources, calculate a relative location for each said display unit relative to other display units within said plurality of display units that form said display area, and extract an applicable portion of said image data from said broadcast data signals based upon said relative location.

20. The panel display system as in claim **19** wherein said display area is substantially rectangular.

21. The panel display system as in claim **19** herein said interconnecting means comprises a panel display connector for electrically coupling said plurality of display units.

22. The panel display system as in claim **21** wherein said panel display connector comprises at least one data/control bus connector and at least one power line connector.

23. The panel display system as in claim **22** wherein said data/control bus connector is fabricated from the group consisting of plastic and metal.

24. The panel display system as in claim **22** wherein said power line connector is fabricated from the group consisting of plastic and metal.

25. The panel display system as in claim **21** wherein said display units have a front face, at least one side face, and a back face, said back face having at least one power port to receive said power line connector and said back face having at least one data/control port to receive said data/control bus connector.

26. The panel display system as in claim **25** wherein said data/control connector has a plurality of protrusions and said data/control port has a plurality of pins extending from said data/control port to be fitted into said plurality of protrusions of said data/control connector.

27. The panel display system as in claim **25** wherein said power line connector has a plurality of protrusions and said power port has a plurality of pins extending from said power port to be fitted into said plurality of protrusions of said power line connector.

28. The panel display system as in claim **19** wherein said image display panel is flat.

29. The panel display system as in claim **19** wherein said image display panel is a liquid crystal display.

30. The panel display system as in claim **19** wherein said image display panel is a light emitting diode.

31. The panel display system as in claim **19** further comprising a nonvolatile memory to store information for said image display panel.